	L #	Hits	Search Text	DBs	Time Stamp
1	L1	360	fpga and (configuration adj memory) and interconnection and (logic adj block)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 10:25
2	L2	254	1 and ("326"/$.ccls. or "716"/$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 10:26
3	L3	139	2 and horizontal and vertical	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/12/09 10:27